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1	2	3	4	5
HIERARCHY		SHEET	PDF-SHEET	A
A5B00900149400-14732_040616		index	sheet 1	B
		netlist1	sheet 2	
		netlist2	sheet 3	
		poslist1	sheet 4	
		page1	---	
		sheet4	sheet 5	
		sheet10	sheet 6	
		sheet11	sheet 7	
		sheet12	sheet 8	
		sheet13	sheet 9	
		sheet14	sheet 10	
		sheet15	sheet 11	
		sheet16	sheet 12	
		sheet17	sheet 13	
		sheet19	sheet 14	
		sheet21	sheet 15	
		sheet22	sheet 16	
		sheet26	sheet 17	
		sheet27	sheet 18	
		sheet35	sheet 19	
		sheet38	sheet 20	
		sheet39	sheet 21	
		sheet42	sheet 22	

				DATE	15.04.2004	<b>R65 Penelope            B2.2 Variant 01 Assembled            TOP HIERARCHY LEVEL            SCHEMATIC</b>	1:1
				USER	Lange		
				APPR'D			
4	15931	16.06.2004	Lge	UC RD ED KLF		<b>71838</b>	index
3	15731	09.06.2004	Lge	Siemens AG			
2	14958	26.05.2004	Lge				
1	14072	21.04.2004	Lge				
VERS.	CHANGE NUMBER	DATE	NAME			( Created by BOM number: A5B00900149424-15931 )	

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Area	Part Number	Description	Sheet Range	Area	Part Number	Description	Sheet Range
A	1.5V_DSP	/sheet13 9 B /sheet11 1 C		B	AC_DCD	/sheet5 7 G /sheet5 10 G	
	1.5V_UC	/sheet13 10 B /sheet11 1 B			AC_RTS	/sheet5 7 G /sheet5 10 G	
	1.5V_UC	/sheet13 11 C			AC_RX	/sheet12 15 E /sheet10 13 E	
					AC_TX	/sheet5 7 G /sheet10 13 E	
					ADV	/sheet22 3 F /sheet12 4 A	
					1.8V_MEM2	/sheet12 14 E /sheet10 6 G	
					AKJU_TYP	/sheet42 4 A /sheet2 5 G	
					A/VDI_CAMERA	/sheet12 2 D /sheet12 6 C /sheet14 1 G	
						/sheet14 1 F /sheet14 1 B	
	2.65V				BATT+		
					BT_RESET	/sheet35 11 D /sheet11 1 C	
					BT_TRST	/sheet42 3 D /sheet13 7 D	
					BT_SIN28M	/sheet26 2 A /sheet13 3 C	
					BT_VCXOEN	/sheet13 2 B /sheet13 3 C	
					BT_WAKEUP_BT	/sheet22 5 F /sheet10 13 F	
C				BT_WAKEUP_GSM	/sheet27 7 D /sheet15 4 E		
				CAMERA_VSYMC	/sheet12 12 C /sheet10 7 G		
				CAM_SENSOR_PD	/sheet5 7 G /sheet5 10 G		
				CAM_SENSOR_RESET	/sheet5 14 B /sheet5 14 B		
				CAS	/sheet12 15 E /sheet10 13 E		
				CHARGE_CTRL	/sheet12 12 C /sheet10 6 G		
				CHARGE_SOURCE	/sheet12 12 C /sheet10 6 G		
				CHARGE_SOURCE	/sheet12 12 C /sheet10 6 G		
				CHARGE_SOURCE	/sheet12 12 C /sheet10 6 G		
				CIF_CLK	/sheet39 1 F /sheet39 9 E		
				CIF_CS	/sheet35 8 D /sheet10 3 C		
				CIF_CS	/sheet35 9 B /sheet10 6 B		
				CIF_CS	/sheet35 9 B /sheet10 6 B		
				CIF_CS	/sheet35 9 B /sheet10 6 B		
	D				CIF_INT	/sheet35 9 A /sheet10 3 F	
				CIF_INT	/sheet35 9 A /sheet10 3 F		
				CIF_MBST	/sheet35 8 D /sheet10 5 B		
				CIF_MTRSR	/sheet35 8 D /sheet10 3 C		
				CIF_RESET	/sheet35 9 A /sheet10 3 C		
				CIF_RESET	/sheet35 9 A /sheet10 3 C		
				CIF_RESET	/sheet35 9 A /sheet10 3 C		
				CIF_RESET	/sheet35 9 A /sheet10 3 C		
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DATE	USER	LANGUE	APPR'D	DATE	NAME
15.04.2004					
16.06.2004	Lge		UC RD,ED,KLF		
09.06.2004	Lge				
20.05.2004	Lge				
21.04.2004	Lge				

VERS. CHANGE NUMBER

R65 Penelope  
B2.2 Variant 01 Assembled  
TOP HIERARCHY LEVEL  
SCHEMATIC

71838

Siemens AG

netlist1

( Created by BOM number: A3600600146424-15931 )

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1	2	3	4	5
	/sheet13 6 B	WAIT		
	VCXOEN_UC	/sheet12 15 E		
	/sheet13 2 C	/sheet12 6 D		
	/sheet10 1 E	/sheet10 6 G		
	VDD_AFC	WR		
	/sheet13 9 C	/sheet12 15 E		
	/sheet11 8 B	/sheet12 12 C		
		/sheet12 7 C		
		/sheet10 7 G		
	VDD_BRIGHT			
	/sheet39 9 A			
	/sheet39 7 C			
	/sheet39 8 E			
	/sheet39 9 A			
	/sheet39 11 A			
	/sheet39 7 A			
	/sheet39 8 B			
	/sheet39 9 B			
	/sheet39 9 E			
	/sheet39 11 C			
	VDD_BRIGHT_ANT			
	/sheet38 7 C			
	/sheet39 6 A			
	VDD_BT			
	/sheet42 4 B			
	/sheet42 3 A			
	/sheet13 9 C			
	/sheet4 14 F			
	VDD_BT_RF			
	/sheet42 3 A			
	/sheet42 7 B			
	/sheet42 6 A			
	/sheet42 5 B			
	VDD_CHARGE			
	/sheet13 2 D			
	/sheet13 10 H			
	VDD_RF1			
	/sheet13 9 C			
	/sheet4 4 D			
	/sheet4 2 D			
	/sheet39 8 F			
	VDD_RF2			
	/sheet13 9 C			
	VDD_RTC			
	/sheet13 2 C			
	/sheet13 9 B			
	/sheet11 5 B			
	VDD_SIM			
	/sheet16 9 C			
	/sheet13 9 B			
	VDD_USB			
	/sheet15 5 F			
	/sheet13 9 B			
	/sheet11 5 B			
	VDD_VIBRA			
	/sheet21 5 C			
	/sheet13 9 B			
	VREFEXS_GND			
	/sheet13 2 E			
	/sheet13 7 H			
	/sheet13 4 F			

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4	15931	16.06.2004	Lge	UC RD ED KLF			
3	15731	09.06.2004	Lge	Siemens AG		71838	netlist2
2	14958	26.05.2004	Lge				
1	14072	21.04.2004	Lge				
VERS.	CHANGE NUMBER	DATE	NAME	( Created by BOM number: A5B00900149424-15931 )			



HIT\_POINTS LOGIK/HF\_PART\_1

HIT\_POINTS LOGIK/HF

400-449



TRANCEIVER ENABLE SIGNAL  
 TRANCEIVER CLOCK SIGNAL  
 TRANCEIVER DATA SIGNAL

Bidirectional analogue RX/TX modulated signal  
 Baseband signals

Reference frequency connection

DCSTX enable Asic for Hittechisolution  
 GSMTX enable Asic for Hittechisolution

analogue power amplifier ramping signal  
 26 MHz reference frequency

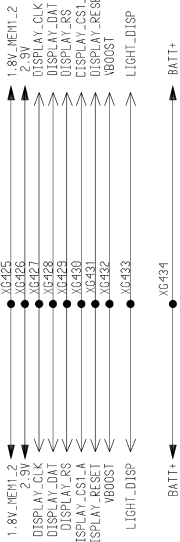
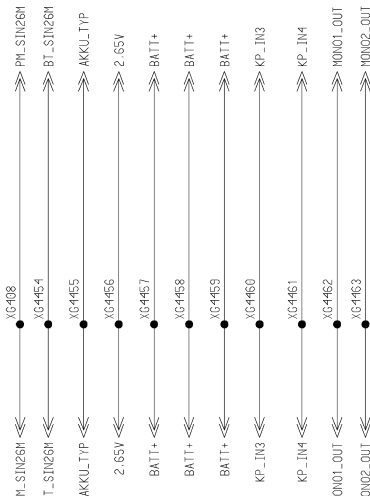
Power up of power controller  
 Temperature sensor X-tal

DETECTION internal/external Antenna

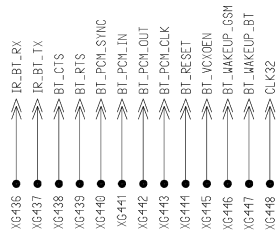
programmed voltage 1

switched LOW / HIGH-band

For Layout



HIT\_POINTS LOGIK/BLUETOOTH

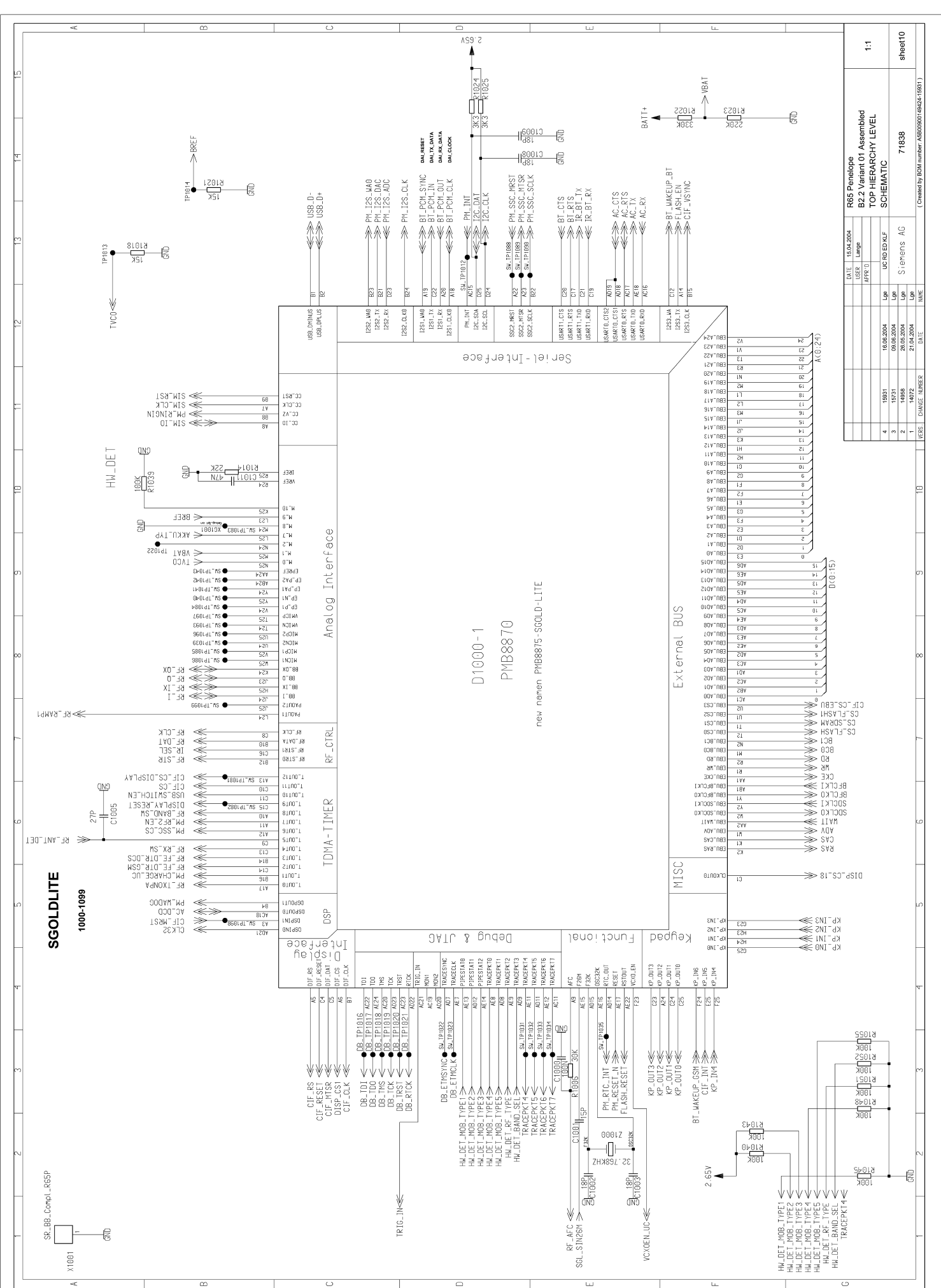


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16.06.2004	UC RD ED KLF	1931
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20.06.2004	1931	2
21.04.2004	1402	1931
21.04.2004	1402	1931

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DATE	USER	Layer
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16.06.2004	Lip	UC RD ED KLF
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20.05.2004	Lip	
21.04.2004	Lip	

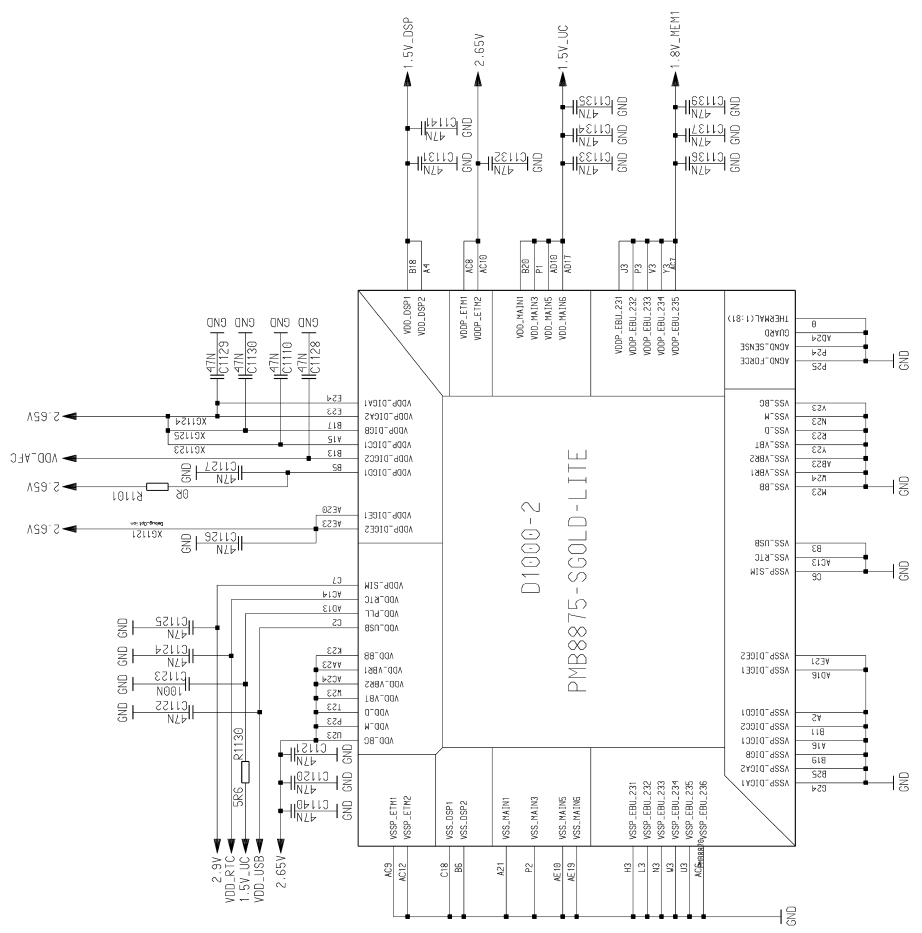
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21.04.2004	1402	

R65 Penelope  
 B22 Variant 01 Assembled  
 TOP HIERARCHY LEVEL  
 SCHEMATIC  
 71638  
 Siemens AG  
 (Created by BOM number: A5509016424.1591)

# SGOLDLITE\_SUPPLYDOMAIN

1100-1199

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DATE	10.04.2004			
USER	Leipziger			
APPROVED				
DATE	16.06.2004	Ug	UC RD ED KLF	
DATE	09.06.2004	Ug		
DATE	26.05.2004	Ug		
DATE	14.02	Ug	Siemens AC	
DATE	21.04.2004	Ug		
DATE	14.02	Ug		
DATE	14.02	Ug		
DATE	14.02	Ug		
DATE	14.02	Ug		

R65 Penelope B2.2 Variant 01 Assembled TOP HIERARCHY LEVEL SCHEMATIC				1:1
4	15931	Ug	UC RD ED KLF	Sheet11
3	19731	Ug		
2	14959	Ug		
71038				
(Created by BOM number: A55059010492615931)				

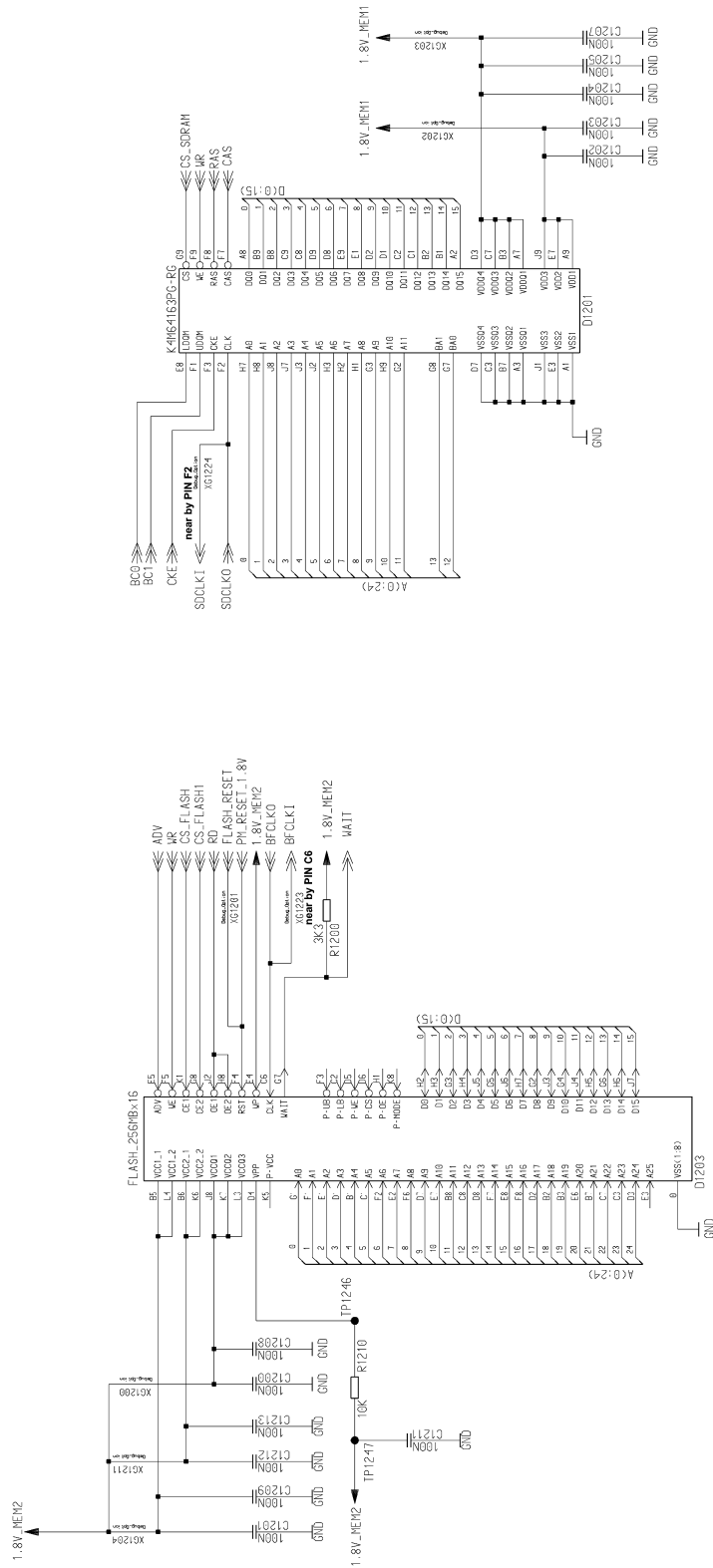
# FLASH\_SDRAM

1200-1289

# SW-TESTPUNKTE

- SA\_IP1820 ● 15
- SA\_IP1821 ● 14
- SA\_IP1822 ● 13
- SA\_IP1823 ● 12
- SA\_IP1824 ● 11
- SA\_IP1825 ● 10
- SA\_IP1826 ● 9
- SA\_IP1827 ● 8
- SA\_IP1828 ● 7
- SA\_IP1829 ● 6
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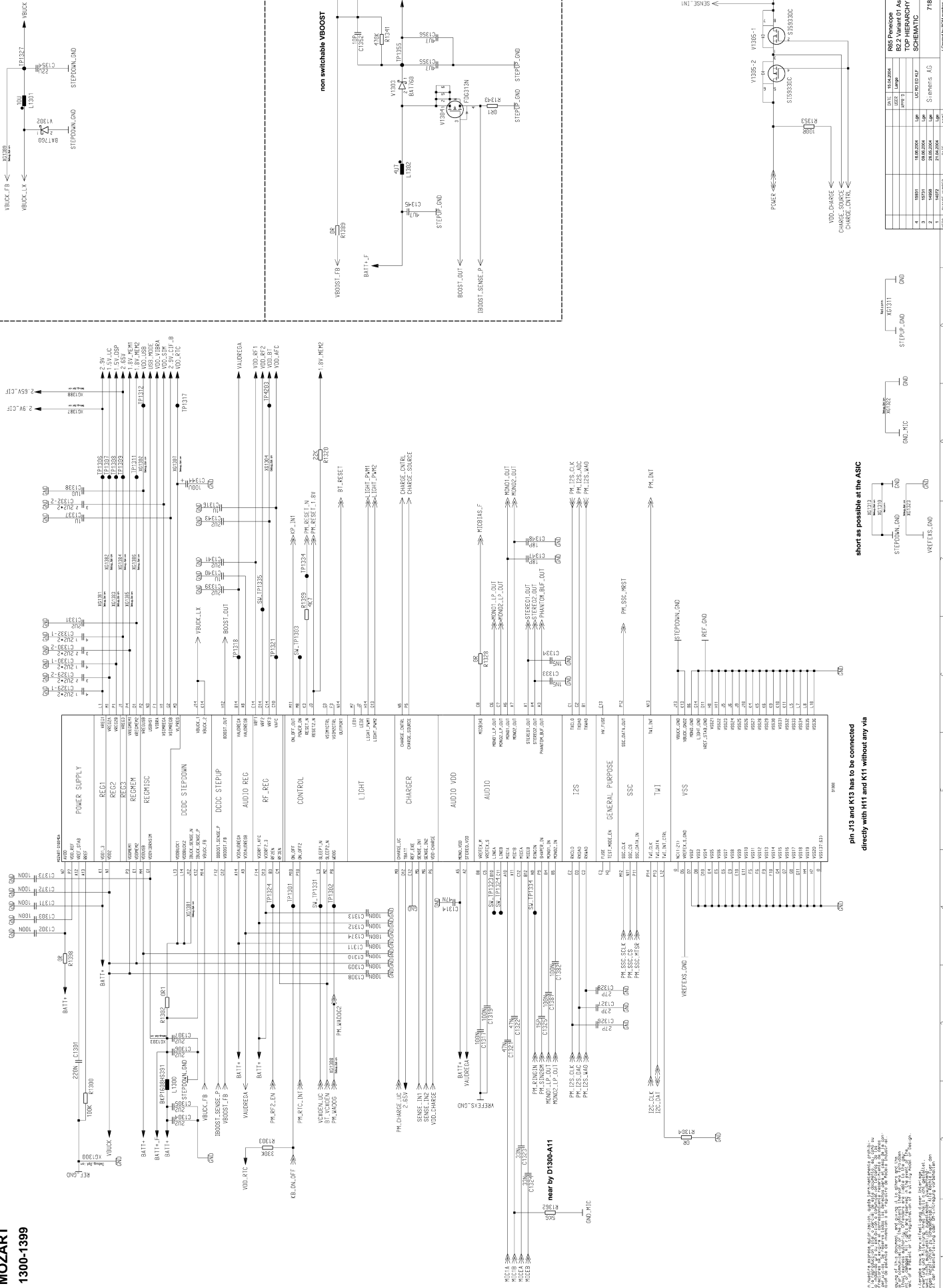


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DATE	16.06.2004
USER	Lange
DATE	09.06.2004
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DATE	20.05.2004
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DATE	14.02.2004
USER	Lange
DATE	21.04.2004
USER	Lange

R65 Penelope  
 B2.2 Variant 01 Assembled  
 TOP HIERARCHY LEVEL  
 SCHEMATIC  
 71838  
 sheet12  
 (Created by BOM number: A58598014842415811)





pin J13 and K13 has to be connected directly with H11 and K11 without any via

short as possible at the ASIC

near by D1300-A11

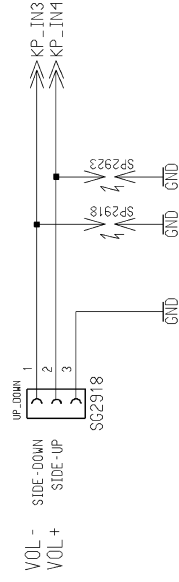
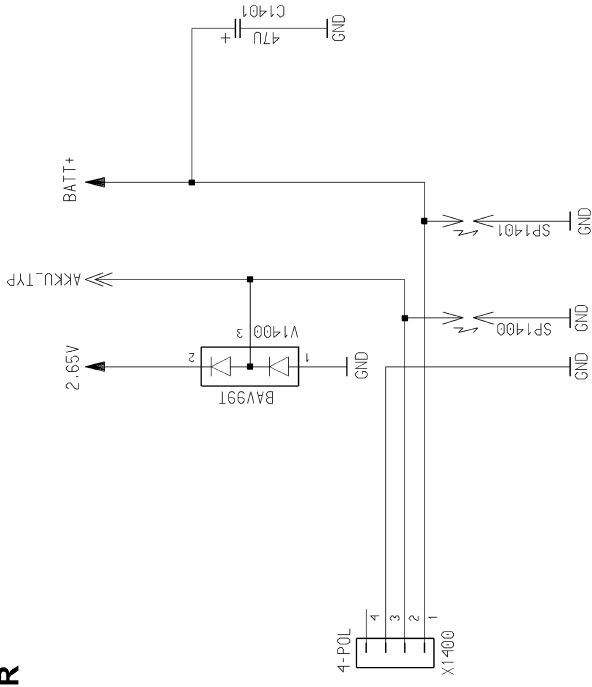
RES Pinout Table:

Pin	Label	Value
1	VDD	
2	VSS	
3	I2S.DAT	
4	I2S.CLK	
5	I2S.ADC	
6	I2S.DAC	
7	I2S.LIN	
8	SSC.DAT	
9	SSC.CLK	
10	SSC.AIN	
11	TUI.DAT	
12	TUI.LIN	
13	PH.CHARGE	
14	PH.CHARGE	
15	PH.CHARGE	
16	PH.CHARGE	
17	PH.CHARGE	
18	PH.CHARGE	
19	PH.CHARGE	
20	PH.CHARGE	
21	PH.CHARGE	
22	PH.CHARGE	
23	PH.CHARGE	
24	PH.CHARGE	
25	PH.CHARGE	
26	PH.CHARGE	
27	PH.CHARGE	
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32	PH.CHARGE	
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34	PH.CHARGE	
35	PH.CHARGE	
36	PH.CHARGE	
37	PH.CHARGE	
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92	PH.CHARGE	
93	PH.CHARGE	
94	PH.CHARGE	
95	PH.CHARGE	
96	PH.CHARGE	
97	PH.CHARGE	
98	PH.CHARGE	
99	PH.CHARGE	
100	PH.CHARGE	

# BATTERY\_CONNECTOR

1400-1449

see sheet 27



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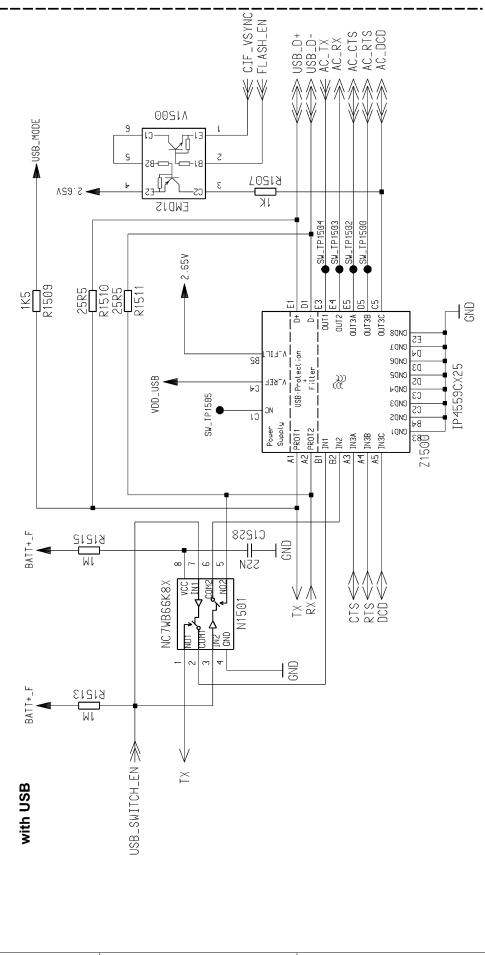
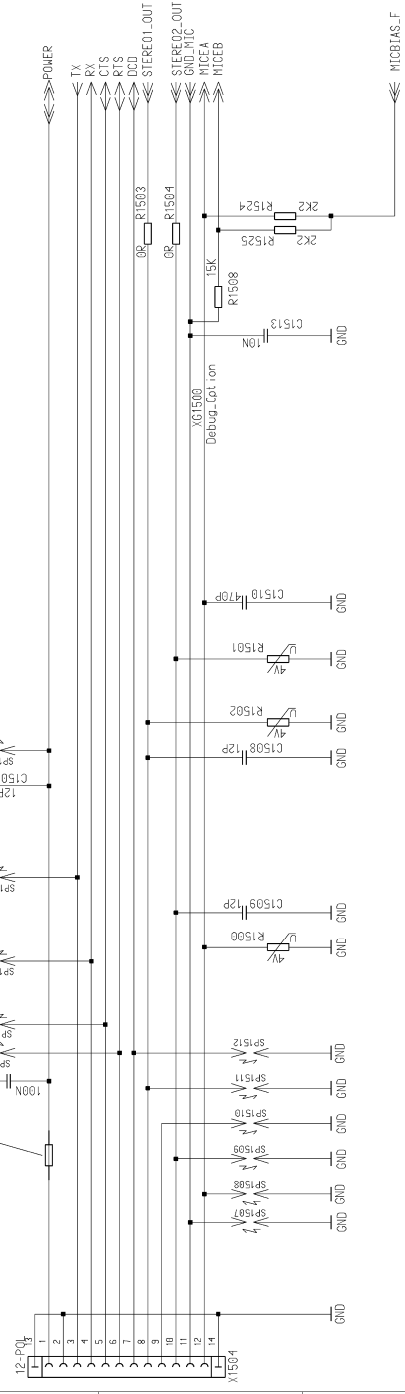
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DATE	15.04.2004	USER	Lange
APPR'D			
UC RD ED	KLF		
VERS.	CHANGE NUMBER	NAME	DATE
4	15831	Lge	16.06.2004
3	15731	Lge	09.06.2004
2	14958	Lge	28.05.2004
1	14072	Lge	21.04.2004

R65 Penelope	71838	1:1
B2.2 Variant 01 Assembled		
TOP HIERARCHY LEVEL		
SCHEMATIC		
Siemens AG		
( Created by BOM number: A5B00600146424-15931 )		

# IO\_CONNECTOR



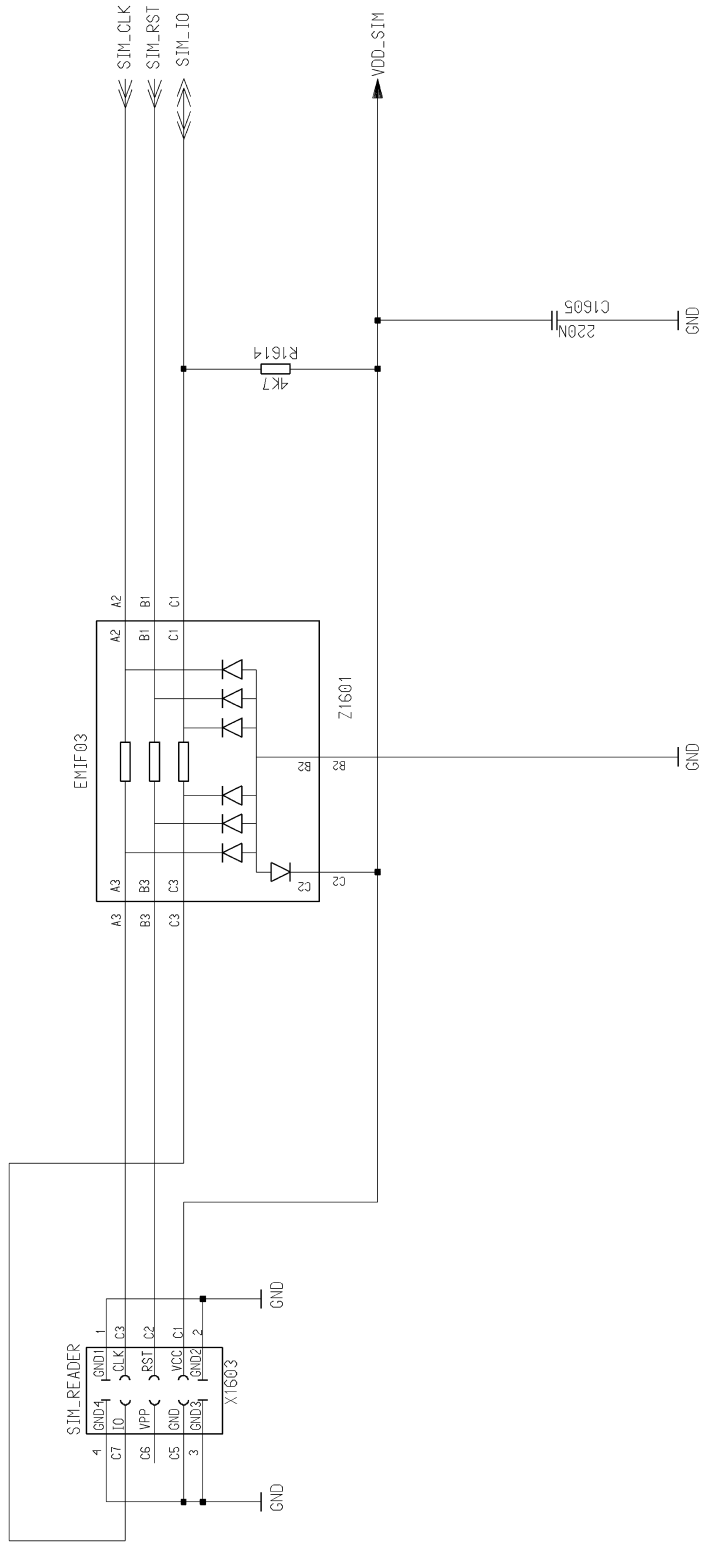
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4	19731	Lige	UC RD ED KLF
3	09.06.2004	Lige	UC RD ED KLF
2	20.05.2004	Lige	UC RD ED KLF
14072	21.04.2004	Lige	UC RD ED KLF
71838	14072	Lige	UC RD ED KLF
DATE <td>16.04.2004</td> <td>USER <td>Lange</td> </td>	16.04.2004	USER <td>Lange</td>	Lange
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4	19731	Lige	UC RD ED KLF
3	09.06.2004	Lige	UC RD ED KLF
2	20.05.2004	Lige	UC RD ED KLF
14072	21.04.2004	Lige	UC RD ED KLF
71838	14072	Lige	UC RD ED KLF

# SIM\_CONNECTOR

1600-1659



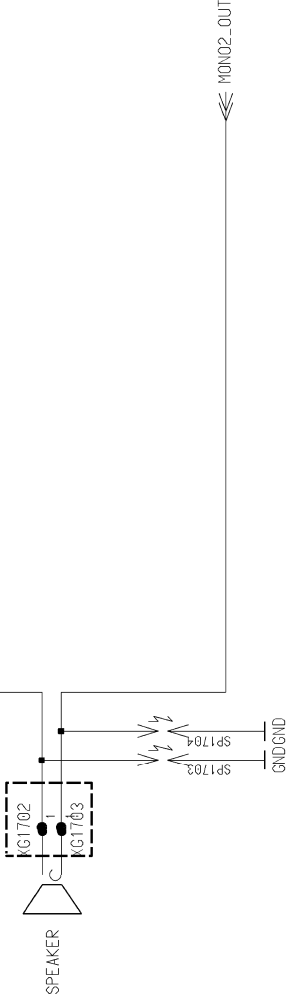
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DATE	15.04.2004	USER	Lange
APPR'D			
UC RD'ED	KLF		
Lge	16.06.2004		
Lge	09.06.2004		
Lge	26.05.2004		
Lge	21.04.2004		
VERS.	CHANGE NUMBER	NAME	DATE
1	14072	Siemens AG	
R65 Penelope			
B2.2 Variant 01 Assembled			
TOP HIERARCHY LEVEL			
SCHEMATIC			
71838			
sheet16			
( Created by BOM number: A5B00600146424-16931 )			

# SPEAKER\_SOLUTION\_1

1700-1759



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DATE	15.04.2004	R65 Penelope	
USER	Lange	B2.2 Variant 01 Assembled	
APPR'D		TOP HIERARCHY LEVEL	
		SCHEMATIC	
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2	14958	Lge	Siemens AG
1	14072	Lge	71838
VERS.	CHANGE NUMBER	DATE	NAME

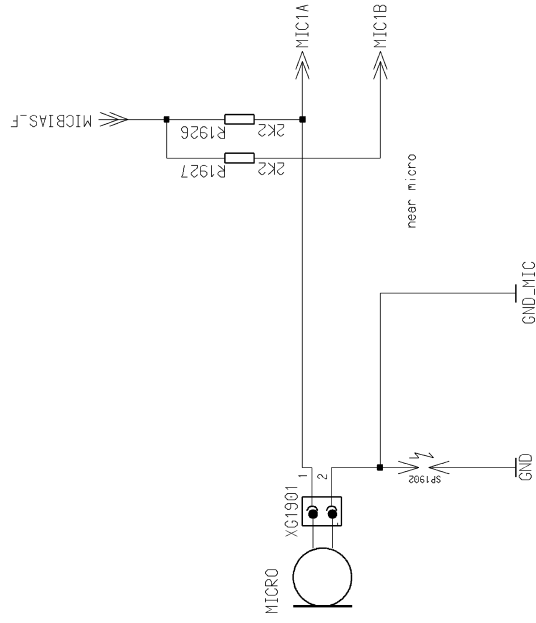
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sheet17

( Created by BOM number: A5B00600146424-16931 )

# MICROPHONE\_CIRCUIT\_1

1900-1999



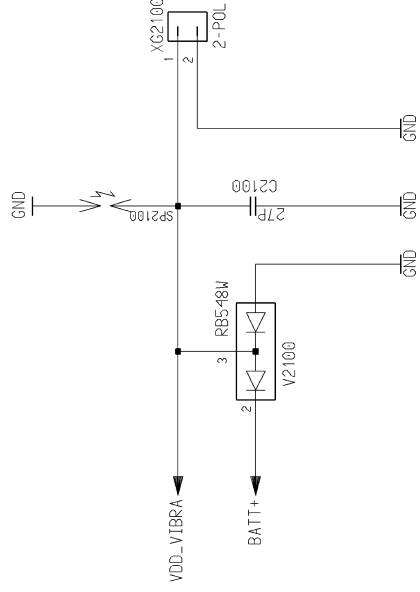
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DATE	15.04.2004	R65 Penelope B2.2 Variant 01 Assembled TOP HIERARCHY LEVEL SCHEMATIC		1:1
USER	Lange			
APPR'D				
4	15831	16.06.2004	Lge	UC RD.ED.KLF
3	15731	09.06.2004	Lge	
2	14958	26.05.2004	Lge	Siemens AG
1	14072	21.04.2004	Lge	71838
VERS.	CHANGE NUMBER	DATE	NAME	sheet19
( Created by BOM number: A5B00600149424-15931 )				

# VIBRA

2100-2125

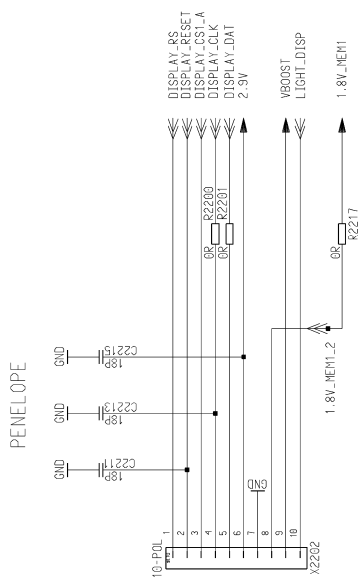


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DATE		15.04.2004		
USER		Lange		
APPR'D				
4	15831	16.06.2004	Lge	UC RD ED KLF
3	15731	09.06.2004	Lge	
2	14958	26.05.2004	Lge	
1	14072	21.04.2004	Lge	
VERS.	CHANGE NUMBER	DATE	NAME	
R65 Penelope B2.2 Variant 01 Assembled TOP HIERARCHY LEVEL SCHEMATIC				
			71838	sheet21
( Created by BOM number: A5B00600146424-16931 )				

# DISPLAY\_CONNECTOR

2200-2299



X2203

DisplayFrame.JLVSSES  
GND

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DATE	16.04.2004	USER	Lange
DATE	19.07.00	USER	UC RD ED KLF
DATE	16.08.2004	USER	Siemens AG
DATE	09.06.2004	USER	
DATE	26.05.2004	USER	
DATE	21.04.2004	USER	
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R65 Penelope  
B2.2 Variant 01 Assembled  
TOP HIERARCHY LEVEL  
SCHEMATIC

1:1

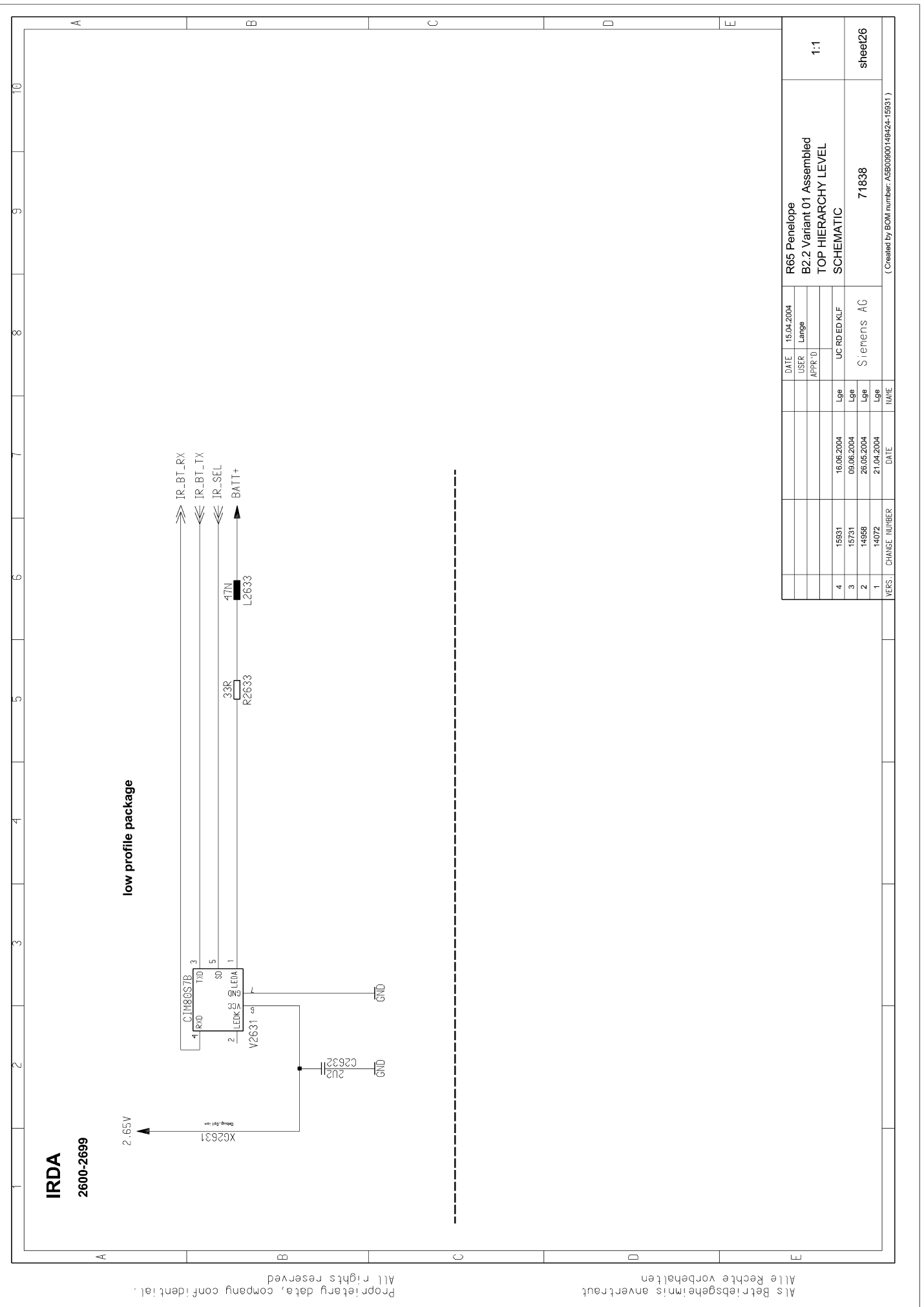
Sheet/22

71938  
(Created by BOM number: A5809901682615931)

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15



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DATE	USER	LANG	APPID
15.04.2004	Lange		

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	16.06.2004		
	09.06.2004		
	26.05.2004		
	21.04.2004		

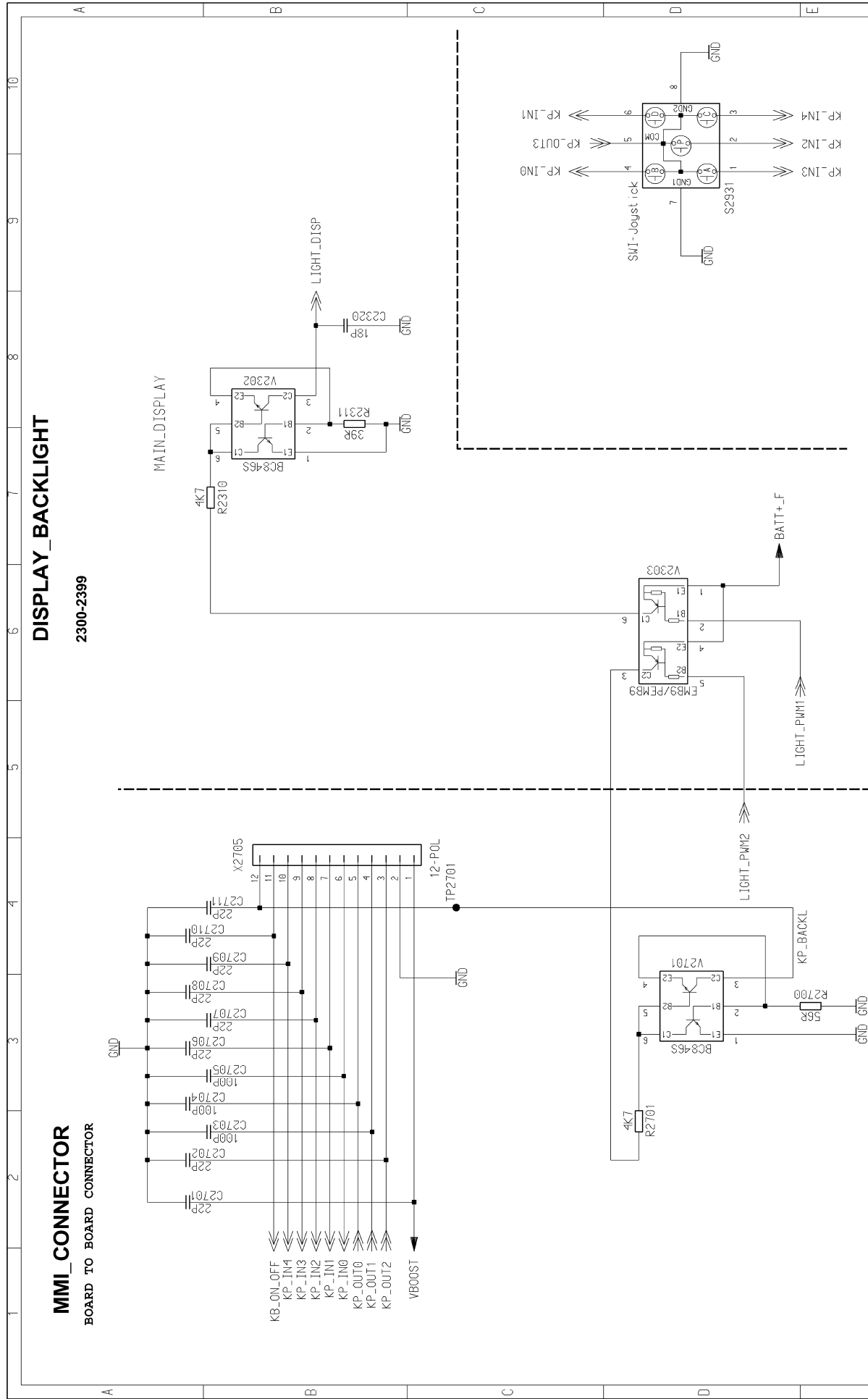
  

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3	15731	09.06.2004	Lge
2	14958	26.05.2004	Lge
1	14072	21.04.2004	Lge

R65 Penelope B2.2 Variant 01 Assembled TOP HIERARCHY LEVEL SCHEMATIC	71838	1:1	sheet26
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( Created by BOM number: A5500600149424-15931 )



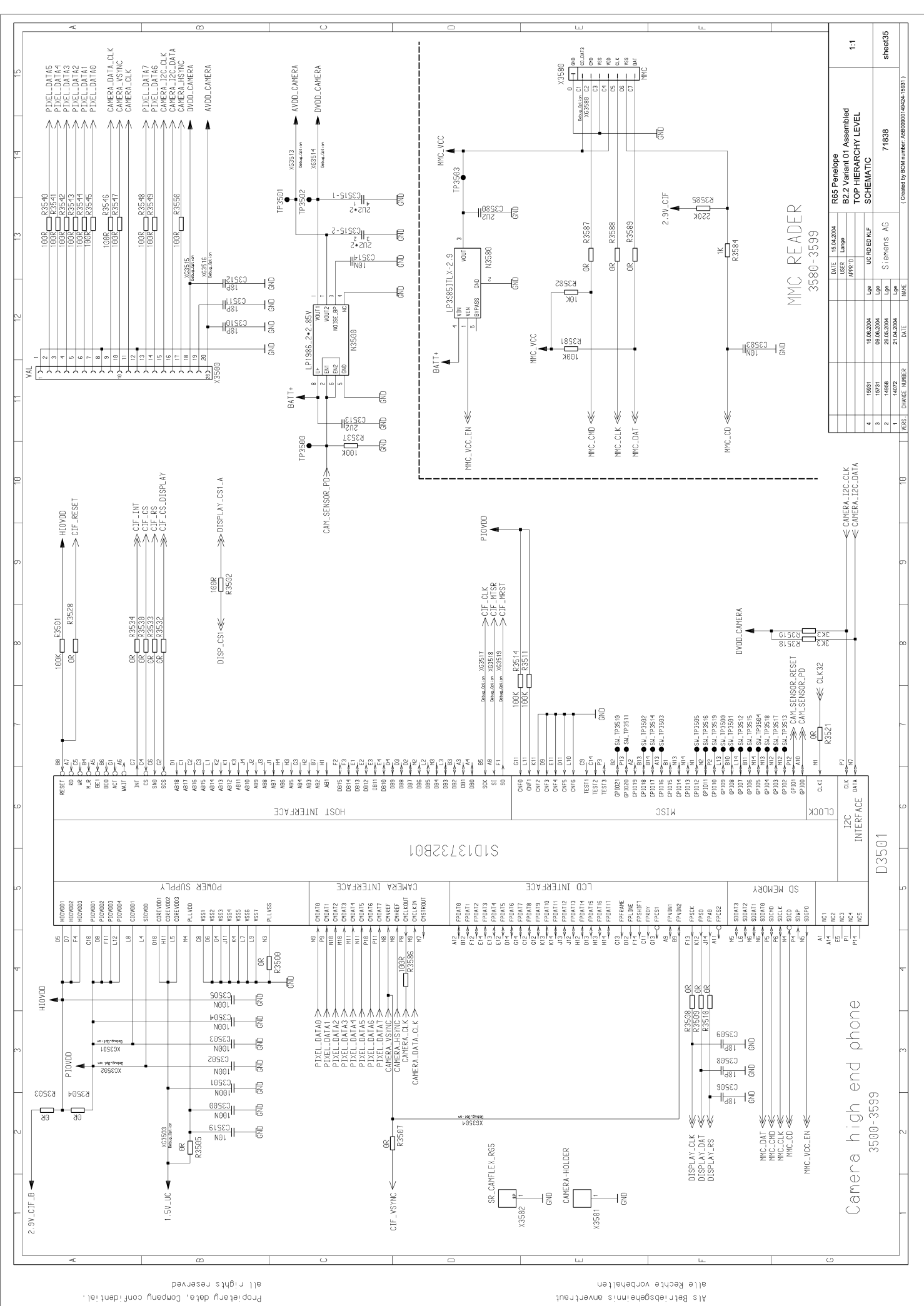
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BOARD TO BOARD CONNECTOR		15.04.2004		16.06.2004		16.06.2004	
		USER: Lange		Lge		Lge	
		APPR'D:		UC RD ED KLF		UC RD ED KLF	
4	15831	16.06.2004	Lge	16.06.2004	Lge	16.06.2004	Lge
3	15731	09.06.2004	Lge	09.06.2004	Lge	09.06.2004	Lge
2	14958	26.05.2004	Lge	26.05.2004	Lge	26.05.2004	Lge
1	14072	21.04.2004	Lge	21.04.2004	Lge	21.04.2004	Lge
VERS.	CHANGE NUMBER	NAME	DATE	NAME	DATE	NAME	DATE

R65 Penelope	1:1
B2.2 Variant 01 Assembled	
TOP HIERARCHY LEVEL	
SCHEMATIC	
71838	sheet27

( Created by BOM number: A5B00600149424-16931 )

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DATE	USER	REVISION
16.08.2004	UC RD ED/KLF	1
09.08.2004	UC RD ED/KLF	2
20.05.2004	UC RD ED/KLF	3
19.03	UC RD ED/KLF	4

11

R65 Penelope  
B2.2 Variant 01 Assembled  
TOP HIERARCHY LEVEL  
SCHEMATIC

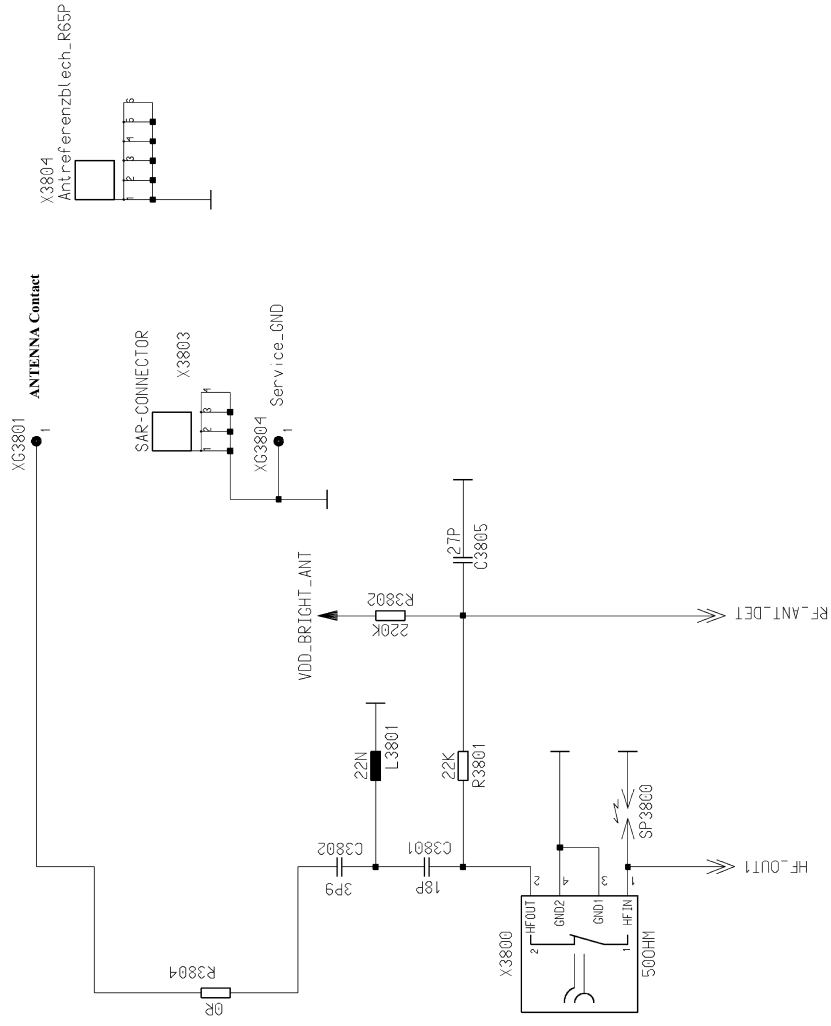
71838

Sheet05

(Created by BOM number: A5505001492415891)

# ANTENNA\_CIRCUIT

## 3800-3849



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APPR'D			
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VERS.	CHANGE NUMBER	DATE	NAME

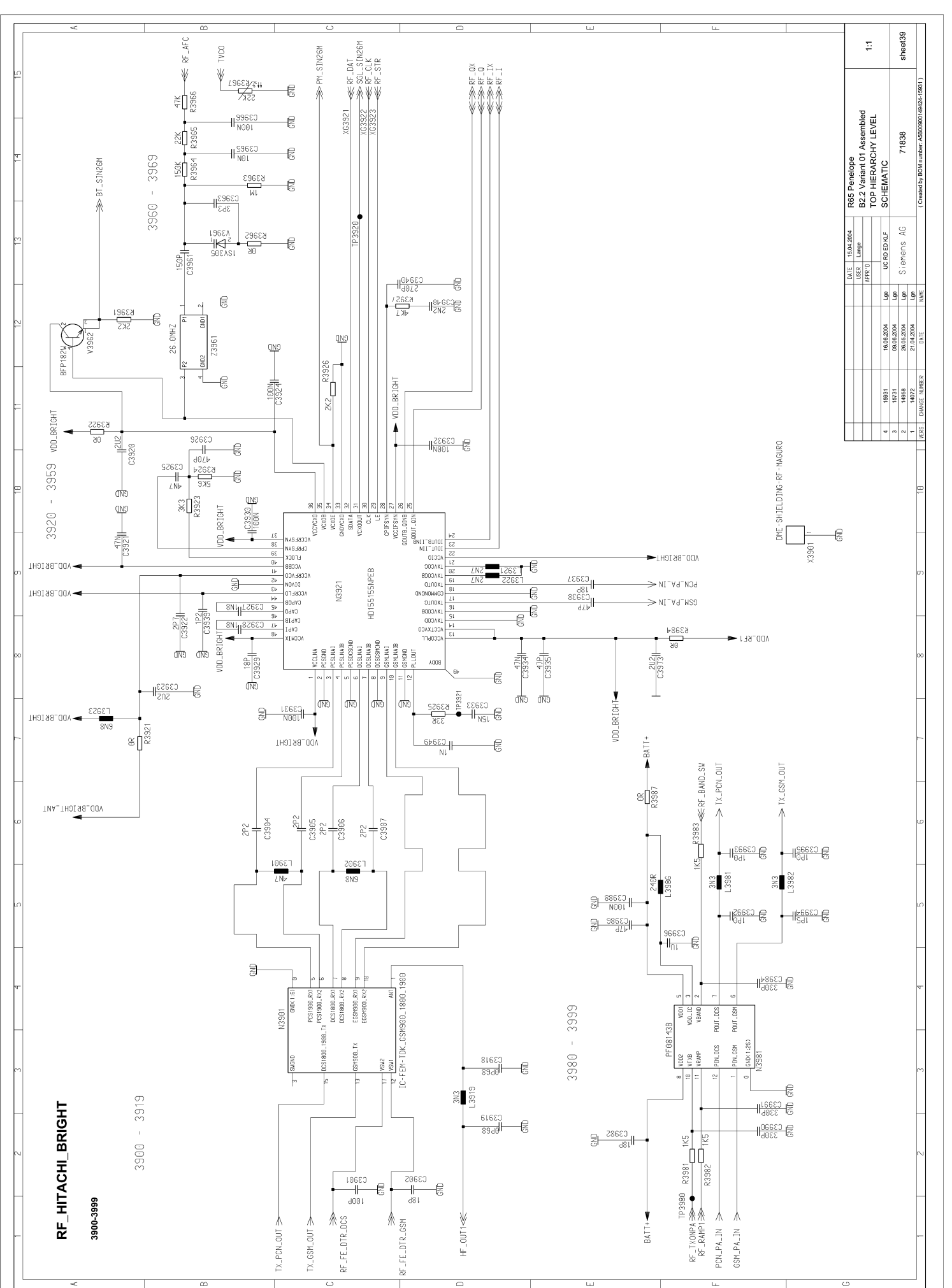
R65 Penelope	71838	sheet38
B2.2 Variant 01 Assembled		
TOP HIERARCHY LEVEL		
SCHEMATIC		
Siemens AG		
( Created by BOM number: A5B00600146424-16931 )		

# RF\_HITACHI\_BRIGHT

3900-3999

3900 - 3919

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3980 - 3999

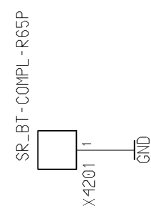
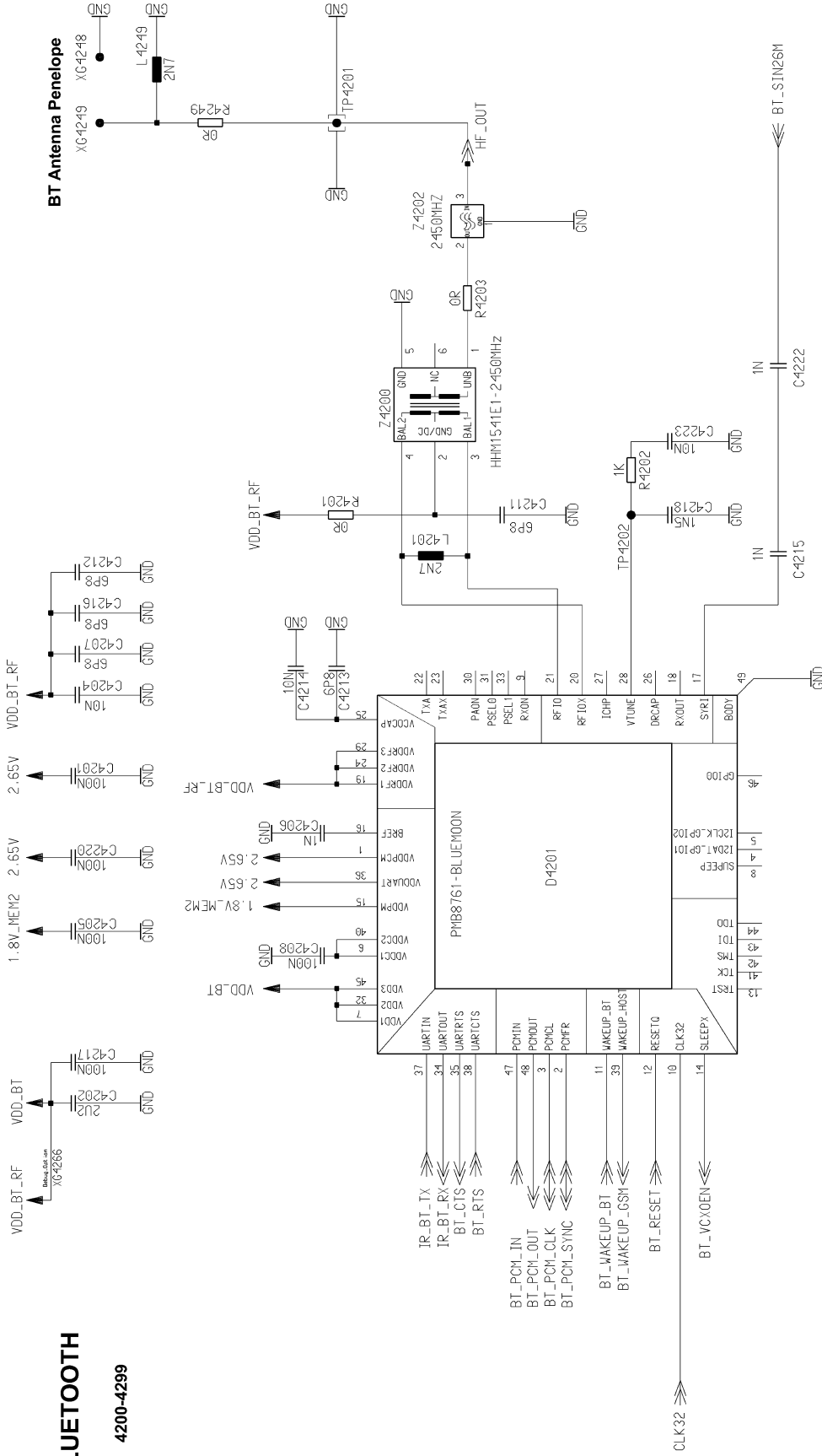
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09.06.2004	Lige		
26.05.2004	Lige		
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21.04.2004	Lige		
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3	19731	09.06.2004	Lige
2	14958	26.05.2004	Lige
1	14072	21.04.2004	Lige

R65 Penelope		B2.2 Variant 01 Assembled		TOP HIERARCHY LEVEL	
SHEMATIC		71838		sheet 69	
(Created by BOM number: AS5059010462615931)					

# BLUETOOTH

4200-4299

# BT Antenna Penelope

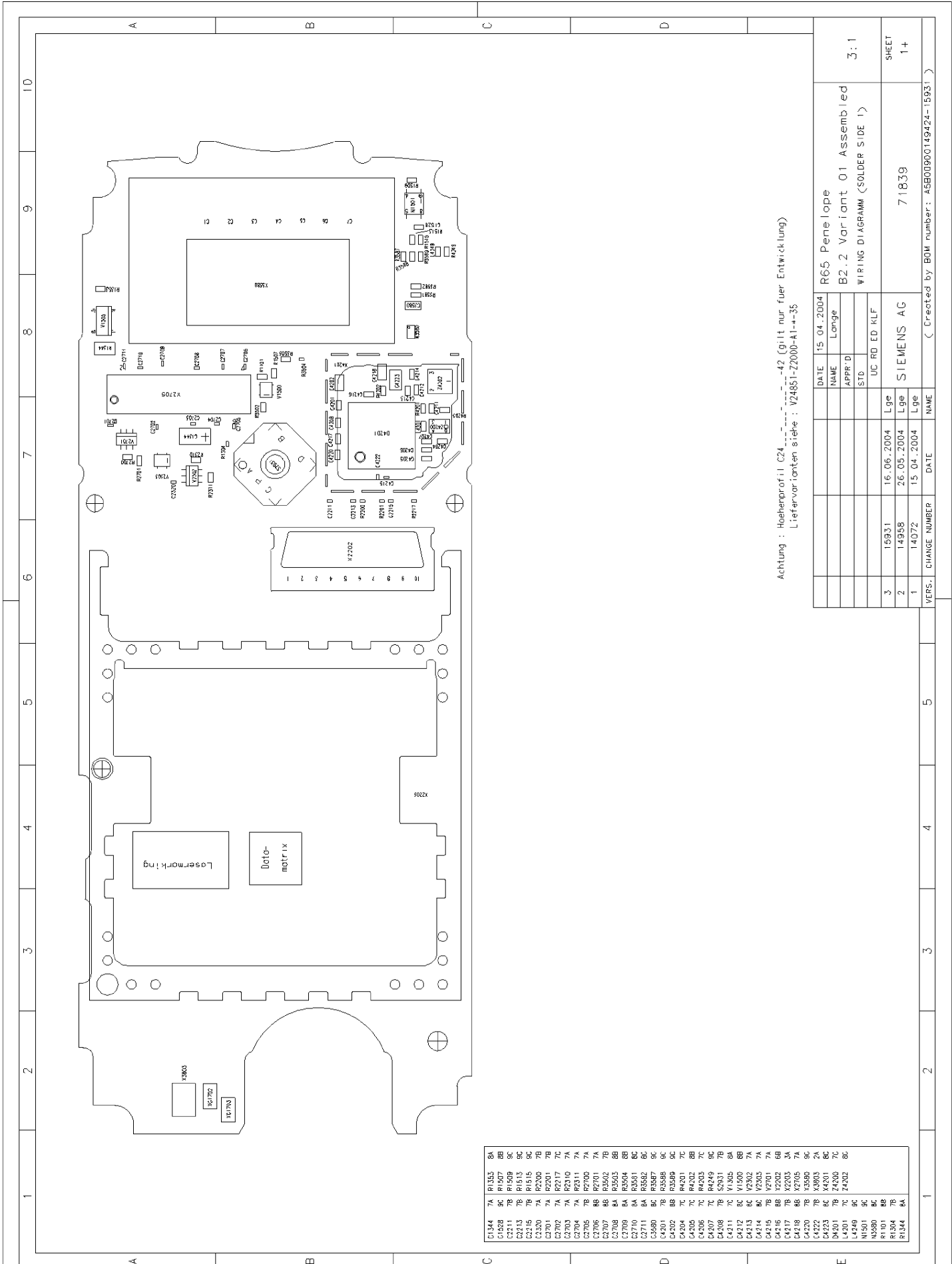


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1	14072	21.04.2004	Lge
VERS.	CHANGE NUMBER	DATE	NAME

R65 Penelope		1:1	
B2.2 Variant 01 Assembled		TOP HIERARCHY LEVEL	
SCHEMATIC		71838	
Siemens AG		sheet42	
( Created by BOM number: A560600146424-15931 )			

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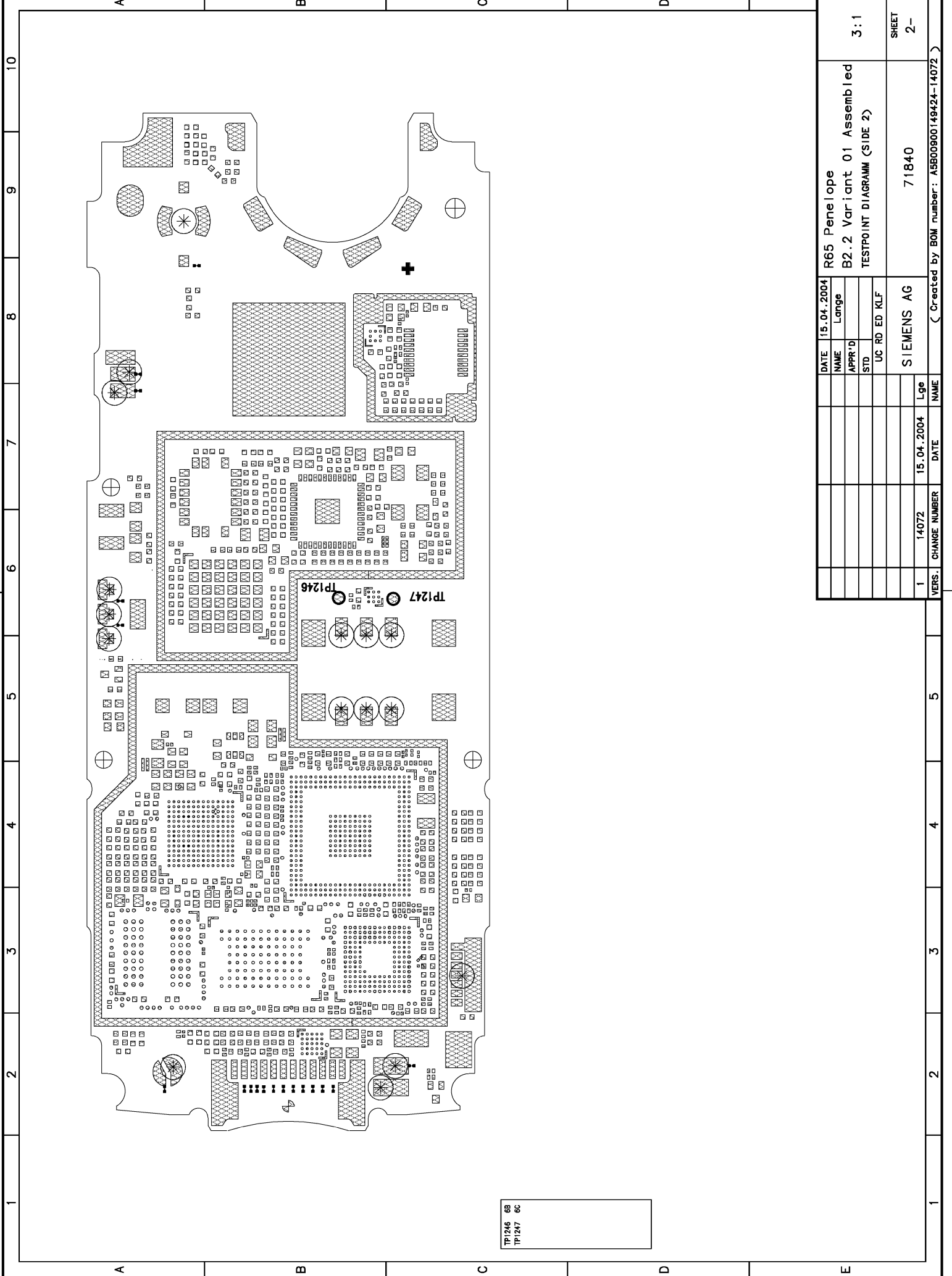
Achtung : Hoehenprofil C24 -----42 (gilt nur fuer Entwicklung)  
 Liefervarianten siehe : V24851-Z2000-41-4-35

C1344	7A	R1353	8A
C1528	9C	R1307	8B
C2111	7B	R1309	9C
C2112	7B	R1415	9C
C3330	7A	R2300	7B
C2701	7A	R2201	7B
C2702	7A	R2217	7C
C2703	7A	R2310	7A
C2704	7A	R2311	7A
C2705	7B	R2700	7A
C2706	8B	R2701	7A
C2707	8B	R3302	7B
C2708	8A	R3303	8B
C2709	8A	R3304	8B
C2710	8A	R3361	8C
C2711	8A	R3562	8C
C3680	8C	R3587	8C
C4201	7B	R3598	9C
C4202	8B	R3599	9C
C4204	7C	R4201	7C
C4205	7C	R4202	8B
C4206	7C	R4203	7C
C4207	7C	R4249	9C
C4208	7B	S2301	7B
C4209	7C	S2302	8B
C4210	7C	M1500	8B
C4211	8C	V2300	7A
C4212	8C	V2300	7A
C4214	8C	V2303	7A
C4215	7B	V2701	7A
C4216	8B	X2202	8B
C4217	7B	X2203	3A
C4218	8B	X2705	7A
C4220	7B	X3690	9C
C4222	7B	X3803	2A
C4223	7C	X4201	9C
C4224	7C	Z4300	7C
C4225	7C	Z4302	8C
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M1501	9C		
N3380	8C		
R1101	8B		
R1304	7B		
R1344	8A		

DATE	15.04.2004	R65 Penelope
NAME	Lange	B2.2 Variant 01 Assembled
APPR'D		3:1
STD		WIRING DIAGRAM (SOLDER SIDE 1)
UC RD ED KLF		
16.06.2004	Lge	71839
26.05.2004	Lge	SIEMENS AG
15.04.2004	Lge	
CHANG NUMBER	DATE	NAME
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14958	26.05.2004	Lge
14072	15.04.2004	Lge
VERF. CHANGE NUMBER DATE NAME		
( Created by BOM number: ASB00900149424-15931 )		







DATE	15.04.2004	R65 Penelope
NAME	Lange	B2.2 Variant 01 Assembled
APPR'D		TESTPOINT DIAGRAM (SIDE 2)
STD		
UC RD ED KLF		
SIEMENS AG	71840	3:1
VERS. CHANGE NUMBER	14072	SHEET
DATE	15.04.2004	2-
Lge		
NAME		
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TP1246 6B  
 TP1247 6C

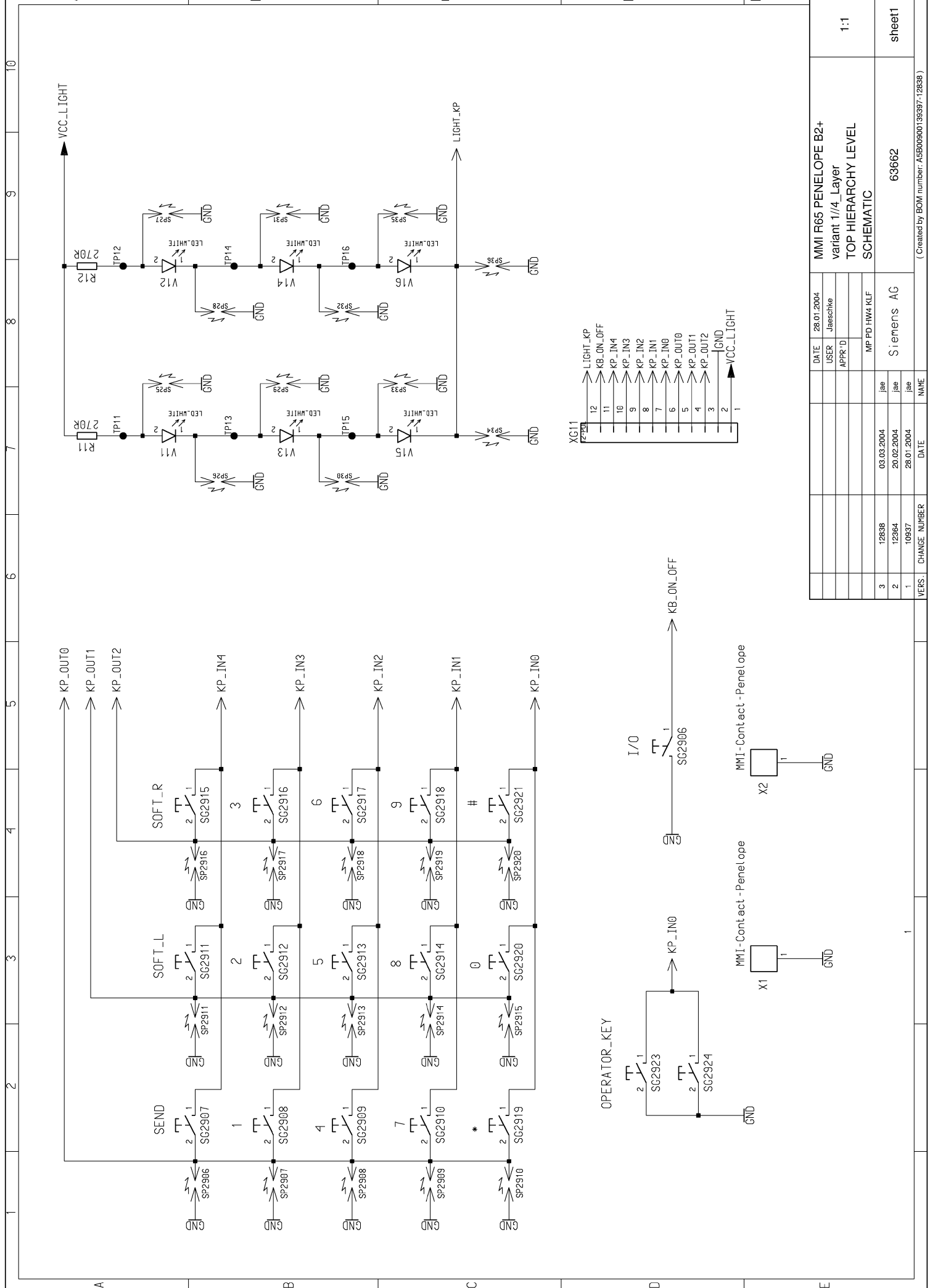


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					B
					C
					D
					E

				DATE	28.01.2004	MMI R65 PENELOPE B2+ variant 1//4_Layer TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	Jaeschke		
				APPR'D			
					MP PD HW4 KLF		
3	12838	03.03.2004	jae	Siemens AG	63662		netlist1
2	12364	20.02.2004	jae				
1	10937	28.01.2004	jae				
VERS.	CHANGE NUMBER	DATE	NAME	( Created by BOM number: A5B00900139397-12838 )			



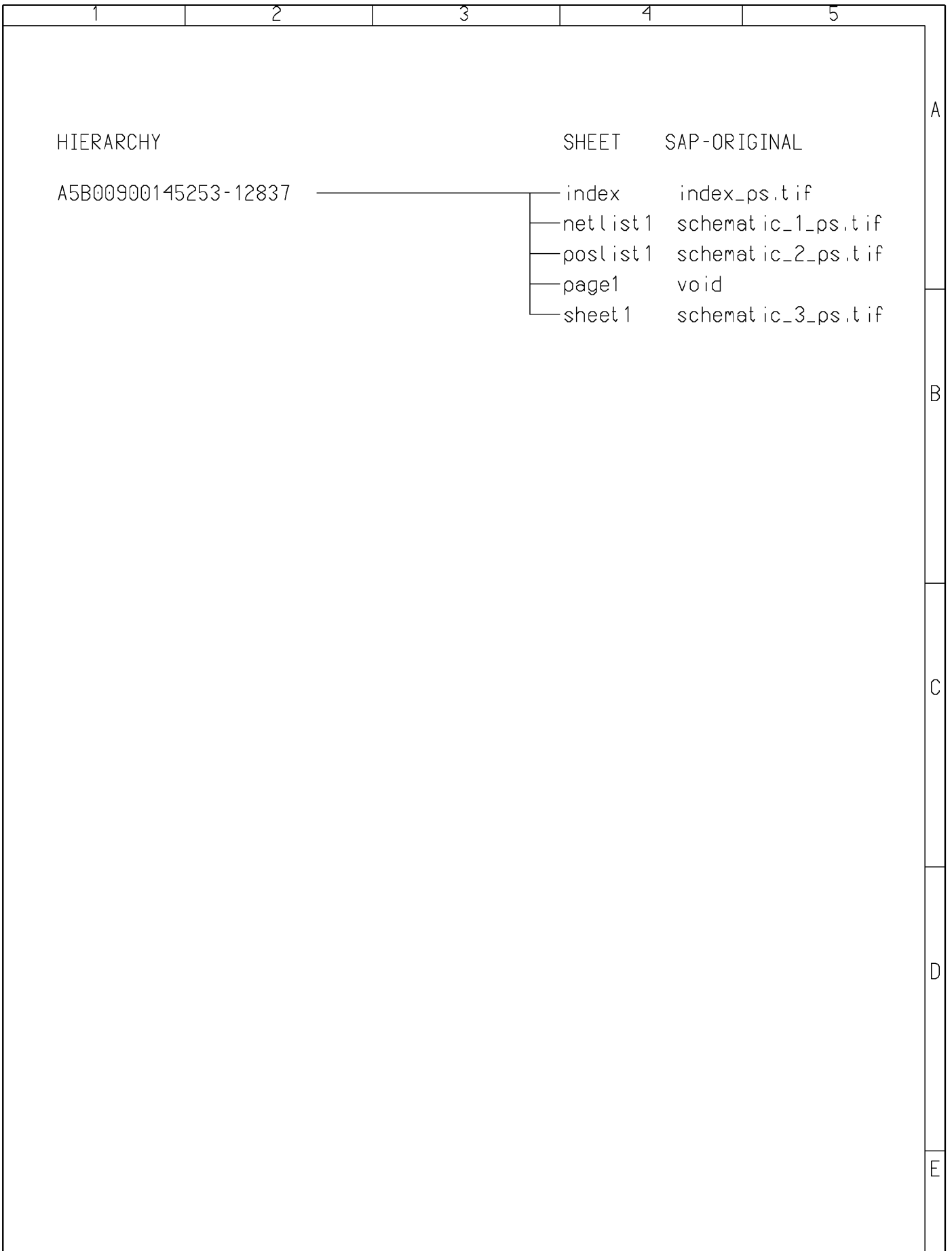
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2	12364	20.02.2004	jae					
1	10637	28.01.2004	jae					
VERS.	CHANGE NUMBER	DATE	NAME					

( Created by BOM number: ASB00900138397-12838 )

MMI R65 PENELOPE B2+  
variant 1/4\_Layer  
TOP HIERARCHY LEVEL  
SCHEMATIC

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			DATE	28.01.2004	MMI R65 PENELOPE B2+ variant 1//4_Layer TOP HIERARCHY LEVEL SCHEMATIC	1:1
			USER	Jaeschke		
			APPR'D			
			MP PD HW4 KLF			
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2	12364	20.02.2004	jae			
1	10937	28.01.2004	jae			
VERS.	CHANGE NUMBER	DATE	NAME	( Created by BOM number: A5B00900139397-12838 )		

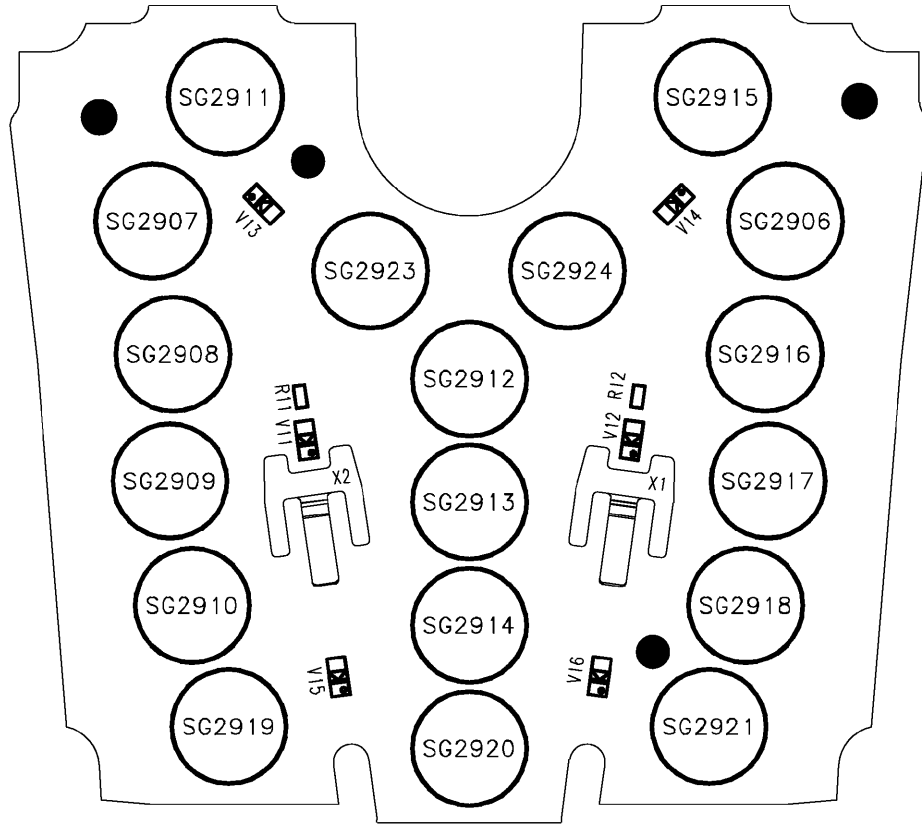
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- R11 /sheet1 7 A
- R12 /sheet1 8 A
- TP16 /sheet1 8 B
- TP12 /sheet1 8 A
- TP14 /sheet1 8 B
- TP15 /sheet1 7 B
- TP11 /sheet1 7 A
- TP13 /sheet1 7 B
- V11 /sheet1 7 A
- V12 /sheet1 8 A
- V13 /sheet1 7 B
- V14 /sheet1 8 B
- V15 /sheet1 7 C
- V16 /sheet1 8 C
- X1 /sheet1 3 E
- X2 /sheet1 5 E

A  
B  
C  
D  
E

				DATE	28.01.2004	MMI R65 PENELOPE B2+ variant 1//4_Layer TOP HIERARCHY LEVEL SCHEMATIC	1:1
				USER	Jaeschke		
				APPR'D			
				MP PD HW4 KLF			
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2	12364	20.02.2004	jae				
1	10937	28.01.2004	jae				
VERS.	CHANGE NUMBER	DATE	NAME	( Created by BOM number: A5B00900139397-12838 )			



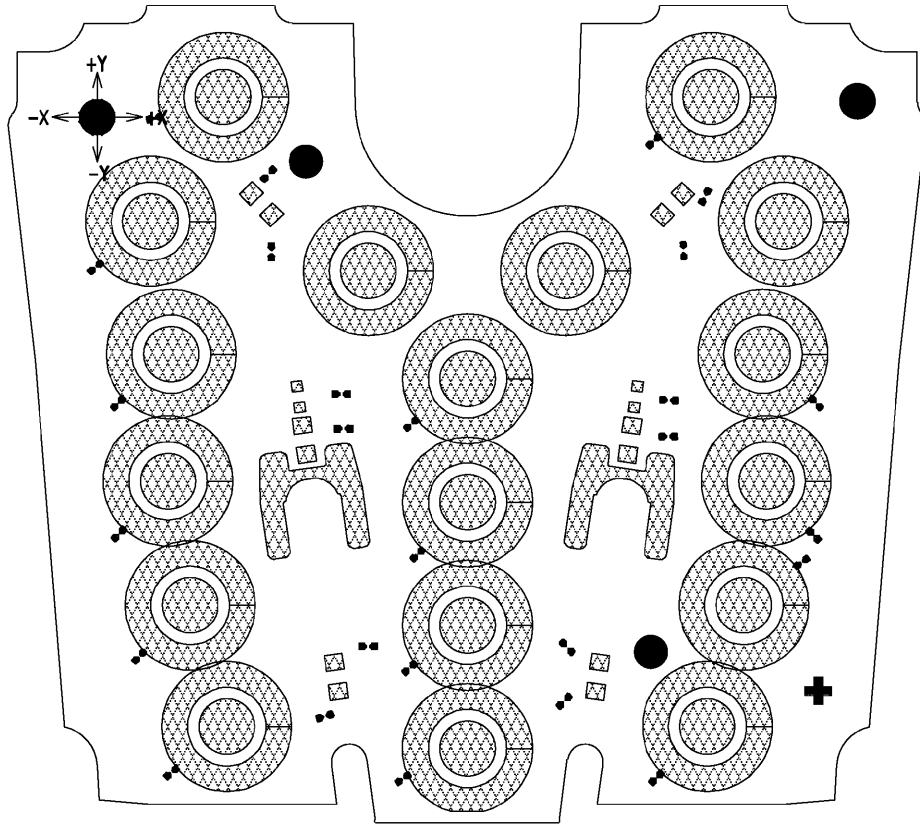
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R12	4B
V11	2B
V12	4B
V13	2A
V14	4A
V15	3B
V16	4B
X1	3B
X2	2B

Achtung : Höhenprofil C24\_\_\_-\_\_\_-\_\_\_-42 (gilt nur fuer Entwicklung)  
Liefervarianten siehe : V24851-Z2000-A1\*-35

				Datum	28.01.2004	MMI R65 PENELOPE B2+ variant 1//4_Layer WIRING DIAGRAMM (SOLDER SIDE 1)	3:1
				Bearb	Jaeschke		
				Gepr.			
				Norm			
				MP PD HW4 KLF			
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2	12364	20.02.2004	jae				
1	10937	28.01.2004	jae				
Zust.	Mitteilung	Datum	Name	( Created by BOM number: A5B00900139397-12838 )			

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A

B

D

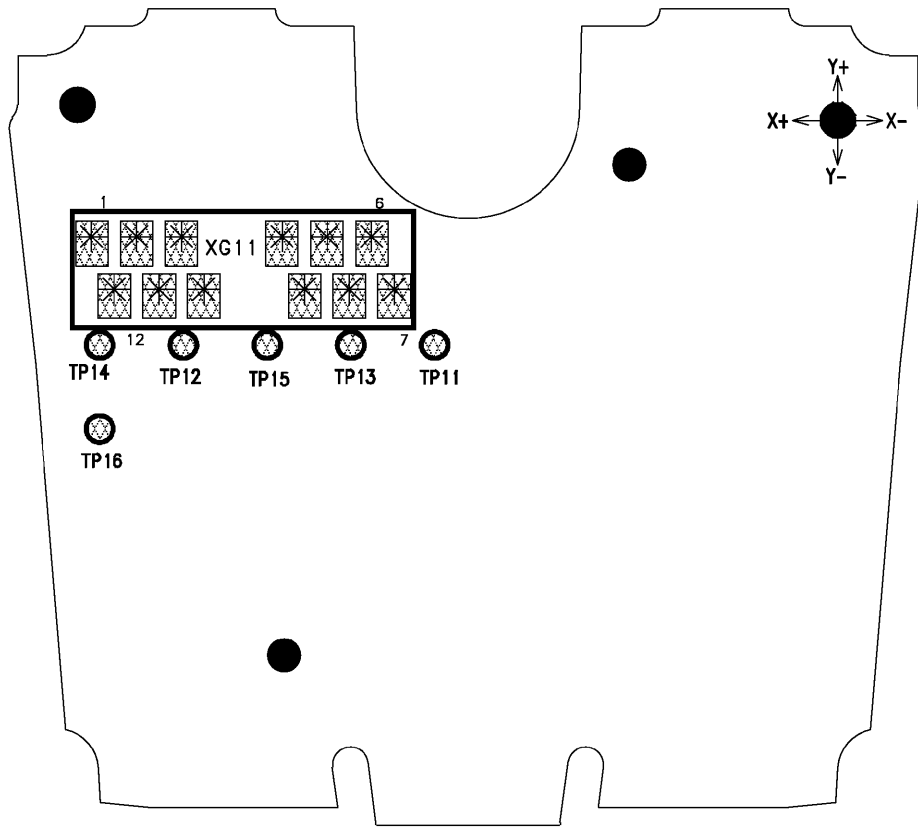
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				Datum	28.01.2004	MMI R65 PENELOPE B2+ variant 1//4_Layer TESTPOINT DIAGRAMM (SIDE 1)	3:1
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				Gepr.			
				Norm			
				MP PD HW4 KLF			
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2	12364	20.02.2004	jae				1+
1	10937	28.01.2004	jae				
Zust.	Mitteilung	Datum	Name	( Created by BOM number: A5B00900139397-12838 )			



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ALLE RECHTE VORBEHALTEN



TP11	3A
TP12	2A
TP13	3A
TP14	2A
TP15	2A
TP16	2B

				Datum	28.01.2004	MMI R65 PENELOPE B2+ variant 1/4_Layer TESTPOINT DIAGRAM (SIDE 2)	3:1
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				Gepr.			
				Norm			
				MP PD HW4 KLF			
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2	12364	20.02.2004	jae				
1	10937	28.01.2004	jae				
Zust.	Mitteilung	Datum	Name	( Created by BOM number: A5B00900139397-12838 )			